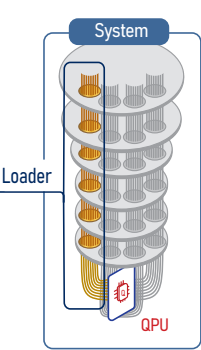




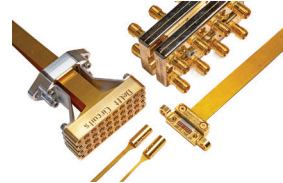
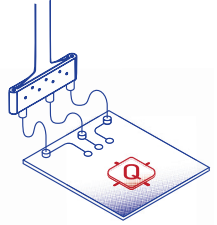
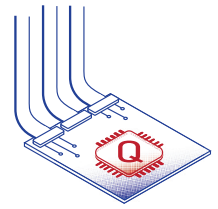
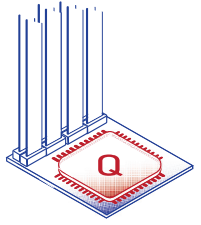
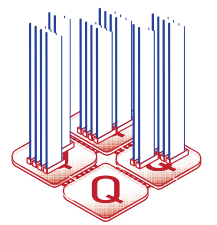
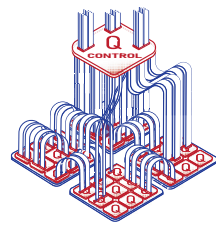


	2019-2024	2025	2026	2027	2028	2029
Guiding principles						
Number of QUBITS per system	≤150	150	350	1.000	5.000	40.000
2 Qubit gate FIDELITY ¹		99.90% ¹	99.95% ¹	1E-5 ²	1E-6 ²	1E-7 ²
Logical qubit ERROR ²						

Quantity		2019-2024	2025	2026	2027	2028	2029
 <p>System</p> <p>Loader</p> <p>QPU</p>	Number of input CHANNELS per loader <i>Number of readout CHANNELS per loader</i>	160 CH/Loader <i>15 CH/Loader ¹</i>	256 CH/Loader <i>32 CH/Loader ¹</i>	1.024 CH/loader <i>128 CH/Loader ¹</i>	4.096 CH/loader <i>512 CH/Loader ¹</i>		
	Number of input CHANNELS per system	 650 CH/sys	 6K CH/sys 1.5K CH/sys	 24K CH/sys 6K CH/sys	 96K CH/sys 24K CH/sys		

Quality	Far-end cross talk (S41)	< -40 dB	< -50 dB	< -60 dB
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Products	Architectural developments	Coaxial RF-connectorization	High Density SMP	Chiplets	Cryo CMOS	Multiplexing	Flex to optical
	Interface milestones						
	Flex milestones	